

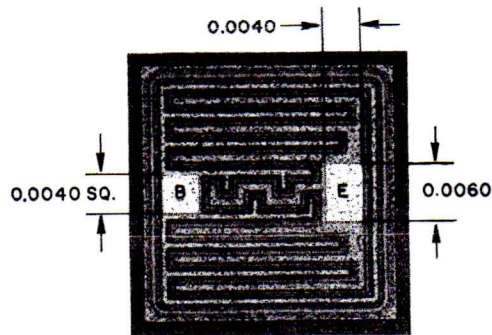


# Sierra Components, Inc.

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Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.



**Top Material: Aluminum**  
**Backside Material: Gold**  
**Bond Pad Size: .004" X .004" min.**  
**Backside Potential: Collector**  
**Mask Ref: DI**

**APPROVED BY: DK**

**DIE SIZE .030 x .030"**

**DATE: 6/15/16**

**MFG: Allegro**

**TICKNESS .015"**

**P/N: BC337**